

# SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

# N-Channel Silicon MOSFET SCH1439 — General-Purpose Switching Device **Applications**

# **Features**

- ON-resistance  $R_{DS}(on)1=55m\Omega(typ.)$
- 4V drive
- · Halogen free compliance

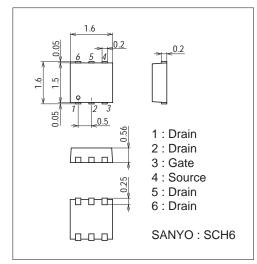
## **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	۱D		3.5	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	14	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm <sup>2</sup> x0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Package Dimensions

unit : mm (typ) 7028-002



#### **Product & Package Information**

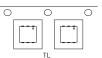
• Package : SCH6

• JEITA, JEDEC

• Minimum Packing Quantity : 5,000 pcs./reel

#### Packing Type : TL

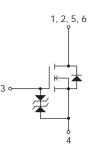






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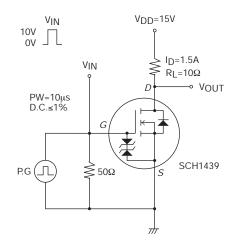
**Electrical Connection** 

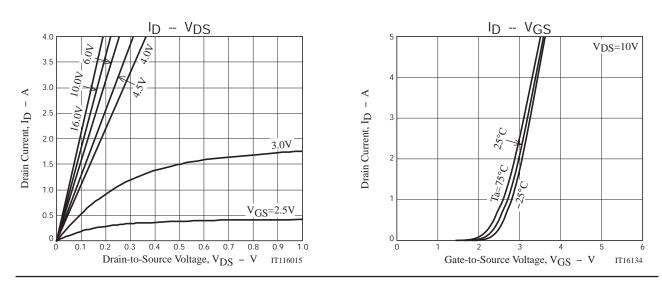


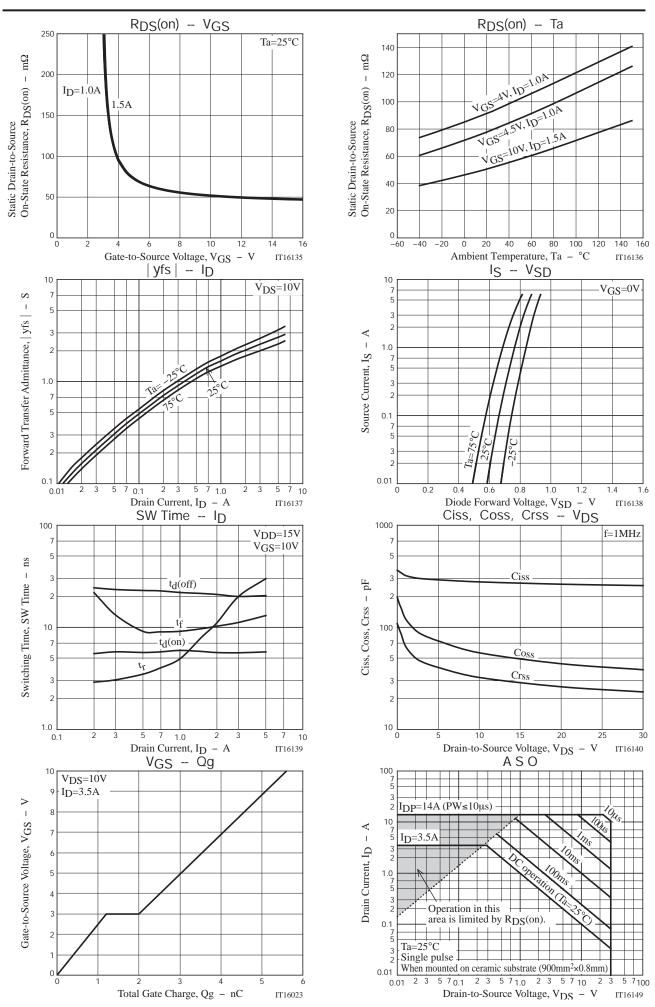
## Electrical Characteristics at Ta=25°C

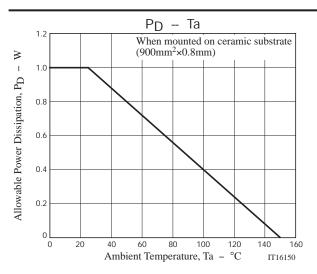
Deservator	Symbol	Conditions	Ratings			1.114
Parameter			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =1.5A		1.8		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	ID=1.5A, VGS=10V		55	72	mΩ
	R <sub>DS</sub> (on)2	ID=1A, VGS=4.5V		78	110	mΩ
	R <sub>DS</sub> (on)3	ID=1A, VGS=4V		91	128	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		280		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		60		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f=1MHz		30		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		5.8		ns
Rise Time	tr	See specified Test Circuit.		8.0		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		21		ns
Fall Time	tf	See specified Test Circuit.		9.7		ns
Total Gate Charge	Qg	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A		5.6		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A		1.2		nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=15V, VGS=10V, ID=3.5A		0.8		nC
Diode Forward Voltage	V <sub>SD</sub>	IS=3.5A, VGS=0V		0.84	1.2	V

### Switching Time Test Circuit









Note on usage : Since the SCH1439 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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